



Test Report: NTS-1200-112

1200W High Reliable True Sine Wave Power Inverter

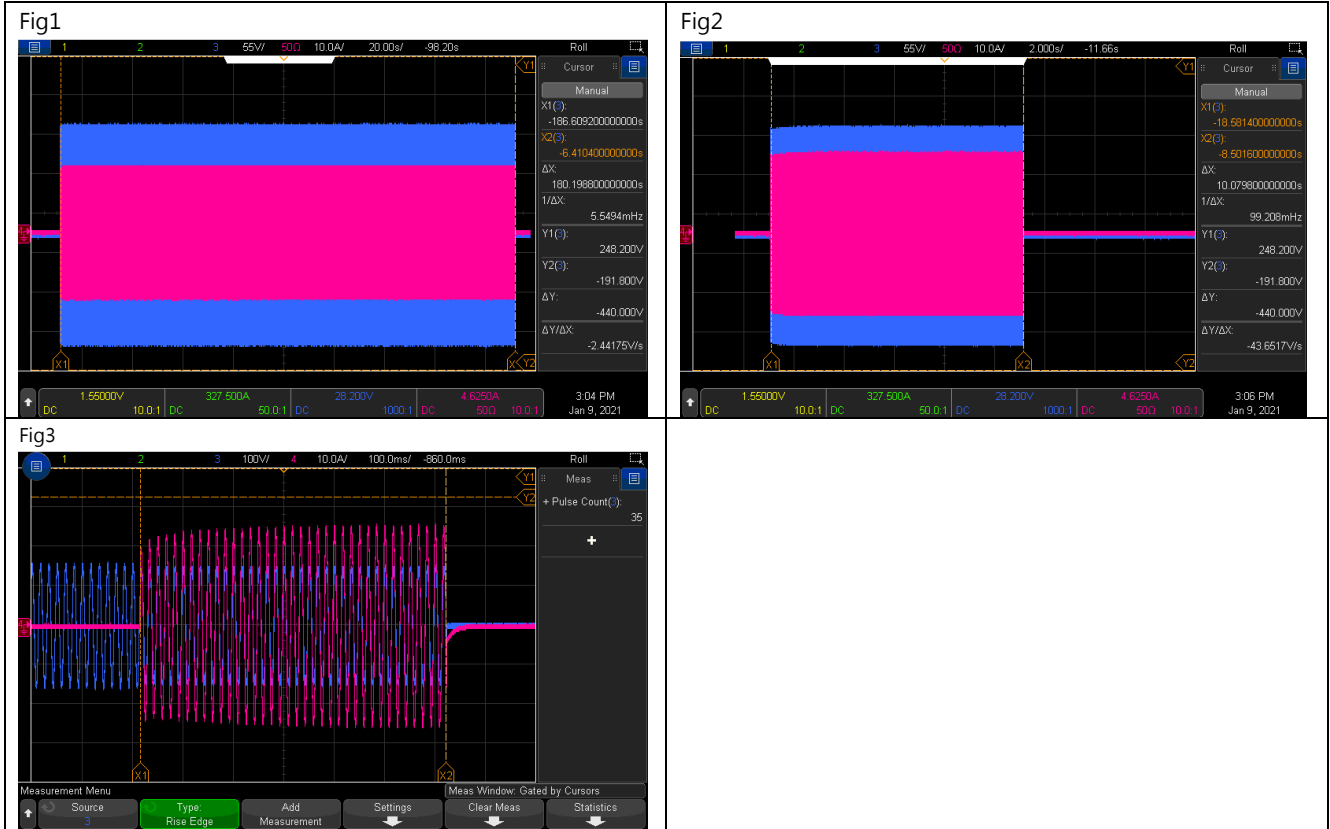
- **DESIGN VERIFY TEST**
 - Output Function Test
 - Input Function Test
 - Protection Function Test
 - Control Function Test
 - APPLICATION Test
 - Component Stress Test
- **SAFETY & E.M.C. TEST**
 - Safety Test
 - E.M.C. Test
- **RELIABILITY TEST**
 - ENVIRONMENT TEST

DESIGN VERIFY TEST

OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	RATED POWER	1200W	IP: 12VDC Ta:25°C	<u>1227</u> W
2	MAXIMUM OUTPUT POWER (TYP)	(1)1380W/180sec. (2)1800w/10sec (3)SURGE POWER 2000W FOR 30CYCLE Vin (30 ± 5 CYCLE)	IP: 12.5VDC OP:TESTING LOAD Ta:25°C	(1) 109.4 V/ 12.29 A/ 180.2 Sec (2) 109.1 V/ 16.01 A/ 10.08 Sec (3) 109.2 V/ 17.98 A/ 35Cycle

CH3:O/P VAC CH4:O/P IAC



3	AC Voltage	100 / 110 / 115 / 120Vac selectable by DIP S.W	IP: 12VDC OP: FULL LOAD Ta:25°C	DIP S.W 100VAC: <u>99.2</u> V DIP S.W 110VAC: <u>119.5</u> V DIP S.W 115VAC: <u>114.5</u> V DIP S.W 120VAC: <u>119.4</u> V
4	FREQUENCY	50/60Hz (±0.1HZ) selectable by DIP S.W	IP: 12VDC OP: FULL LOAD Ta:25°C	DIP S.W 50HZ: <u>50.041</u> HZ DIP S.W 60HZ: <u>59.958</u> HZ

5	WAVEFORM	True sine wave (THD < 3%)	IP: 12.5VDC OP: 75% LOAD (900W) (1) Vo(min) (2) Vo(nor) (3) Vo(max) Ta: 25°C	(1) 1.91 % / Vo(min) / 75% LOAD (2) 1.91 % / Vo(nor) / 75% LOAD (3) 1.62 % / Vo(max) / 75% LOAD
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CH3:O/P VAC CH4:O/P IAC

Fig1

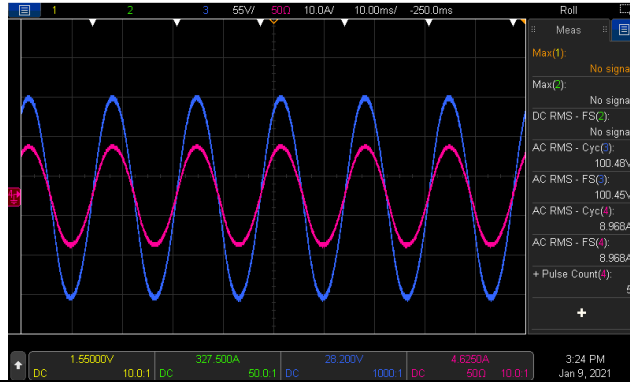


Fig2

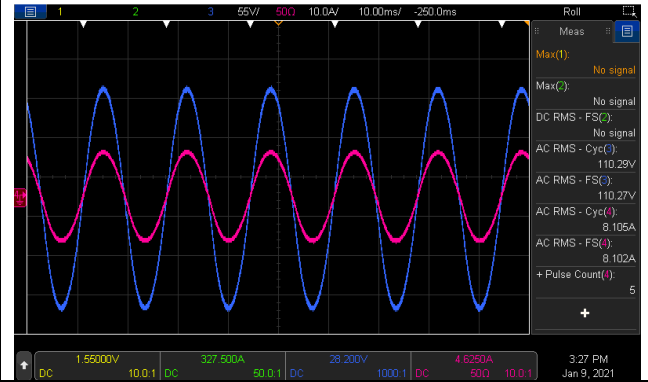
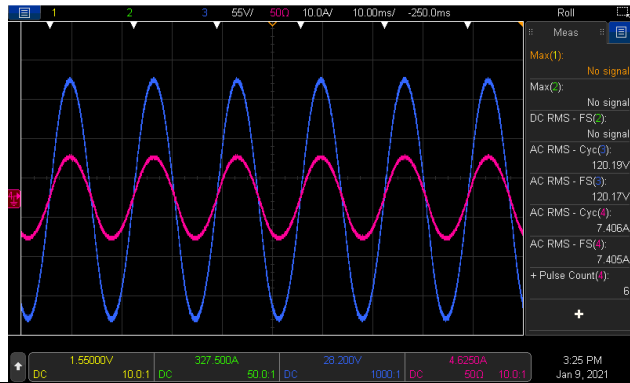
































Fig3



6	AC REGULATION	±3%	IP: 12.5VDC OP: 75% LOAD (900W) Ta: 25°C	0.56 %
7	Overshoot /Undershoot	< ±10%	IP: 12VDC OP: (1) full load turn on (2) no load turn on (3) full /no load change Ta: 25°C	(1) -6.64 % (2) 1.2 % (3) -3.28 %
8	O/P voltage DC offset	Vin(nor)= 12 v · Vo < 200mV · no load : 119.1 mV / full load: 122.2mV		

9	LED STATUS	<ul style="list-style-type: none"> Status test <table border="1"> <thead> <tr> <th>LED</th> <th>Status</th> <th>RESULT</th> </tr> </thead> <tbody> <tr> <td>Green</td> <td> Inverter OK</td> <td>OK</td> </tr> <tr> <td>Orange</td> <td> Remote off  Saving mode</td> <td>OK</td> </tr> <tr> <td>Red</td> <td> Abnormal Status (See SPEC)</td> <td>OK</td> </tr> </tbody> </table> Battery test <table border="1"> <thead> <tr> <th>LED</th> <th>Battery RANGE</th> <th>RESULT</th> </tr> </thead> <tbody> <tr> <td>Green </td> <td>12.5~15.5 Vdc±0.3v</td> <td>12.57Vdc ~ 15.52Vdc</td> </tr> <tr> <td>Orange </td> <td>11~ 12.5Vdc ±0.3v</td> <td>11.07Vdc ~ 12.51Vdc</td> </tr> <tr> <td>Red </td> <td><11.0 Vdc ±0.3v > 15.5vdc±0.3v</td> <td>< 11.1 Vdc > 15.67 Vdc</td> </tr> </tbody> </table> Load test <table border="1"> <thead> <tr> <th>LED</th> <th>LOAD RANGE</th> <th>RESULT</th> </tr> </thead> <tbody> <tr> <td>Green </td> <td>Min. load ~ 40%±5% LOAD</td> <td>Min. load ~38.2%</td> </tr> <tr> <td>Orange </td> <td>40%±5% ~ 80%±5% LOAD</td> <td>41.1%~ 77.2%</td> </tr> <tr> <td>Red </td> <td>≥ 80%±5% LOAD</td> <td>≥ 79.3%</td> </tr> </tbody> </table> 	LED	Status	RESULT	Green	 Inverter OK	OK	Orange	 Remote off  Saving mode	OK	Red	 Abnormal Status (See SPEC)	OK	LED	Battery RANGE	RESULT	Green 	12.5~15.5 Vdc±0.3v	12.57Vdc ~ 15.52Vdc	Orange 	11~ 12.5Vdc ±0.3v	11.07Vdc ~ 12.51Vdc	Red 	<11.0 Vdc ±0.3v > 15.5vdc±0.3v	< 11.1 Vdc > 15.67 Vdc	LED	LOAD RANGE	RESULT	Green 	Min. load ~ 40%±5% LOAD	Min. load ~38.2%	Orange 	40%±5% ~ 80%±5% LOAD	41.1%~ 77.2%	Red 	≥ 80%±5% LOAD	≥ 79.3%
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INPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	VOLTAGE RANGE (TYP)	10VDC~16.5VDC	IP: TESTING OP:NO LOAD/FULL LOAD Ta:25°C I/P: LOW-LINE=10.5V HIGH-LINE=16.2V O/P:FULL/MIN LOAD (PLEASE CHECK DERATING CURVE) ON:30Sec/OFF:30Sec 10MIN (POWER ON/OFF NO DAMAGE) I/P: 12V O/P:FULL LOAD ON:30ec OFF:30ec 12Hr (POWER ON/OFF NO DAMAGE)	<u>10.09</u> VDC~ <u>16.56</u> VDC/NO LOAD <u>10.11</u> VDC~ <u>16.56</u> VDC/FULL LOAD Test: <u>OK</u>

2	DC CURRENT (TYP)	120A	IP: 12VDC OP: FULL LOAD Ta: 25°C	<u>115.3</u> A
3	NO LOAD DISSIPATION (Typ.)	$\leq 1.2W$ @standby saving mode $\leq 15W$ @NON-Saving Mode	IP: 48VDC OP: NO LOAD Ta: 25°C	<u>1.149</u> W <u>10.44</u> W
4	SAVING MODE TO NORMAL	$P_o \geq 25W$	IP: 12VDC OP: TESTING LOAD Ta: 25°C	<u>≥ 19</u> W
5	NORMAL TO SAVING MODE	$P_o \leq 10W$	IP: 12VDC OP: TESTING LOAD Ta: 25°C	<u>≤ 12.5</u>
6	OFF MODE CURRENT DRAW (Typ.)	$\leq 1mA$	IP: 12VDC OP: Sw off Ta: 25°C	<u>0.42</u> mA
7	EFFICIENCY(TYP)	900W/89%	IP: 12.5VDC OP: $P_o=900W$ 110V/60HZ (factory setting) Ta: 25°C	<u>89.5</u> %

PROTECTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	BAT LOW ALARM	11V \pm 0.3VDC	IP: TESTING OP: FULL LOAD SW: ON Ta: 25°C	<u>11.01</u> V
2	BAT LOW SHUT DOWN	10V \pm 0.3VDC	IP: TESTING OP: FULL LOAD SW: ON Ta: 25°C	<u>10.11</u> V
3	BAT LOW RESTART	12.5V \pm 0.3VDC	IP: TESTING OP: FULL LOAD SW: ON Ta: 25°C	<u>12.56</u> V
4	BAT HIGH ALARM	15.5V \pm 0.3VDC	IP: TESTING OP: FULL LOAD SW: ON Ta: 25°C	<u>15.57</u> V
5	BAT HIGH SHUT DOWN	16.5V \pm 0.3VDC	IP: TESTING OP: FULL LOAD SW: ON Ta: 25°C	<u>16.57</u> V
6	BAT HIGH RESTART	15V \pm 0.3VDC	IP: TESTING OP: FULL LOAD SW: ON Ta: 25°C	<u>15.09</u> V

7	OVER TEMPERATURE	Shut down o/p voltage: re-power on	IP: HI LINE/LOW-LINE OP: FULL LOAD SW:ON Ta:25°C	Shut down o/p voltage, re-power on to recover LED DISPLAY: <u> OK </u>
8	OUTPUT SHORT	Shut down o/p voltage: re-power on	IP: 12VDC O/P: FULL LOAD SW:ON Ta:25°C	Shut down o/p voltage, re-power on to recover LED DISPLAY: <u> OK </u> (1).TEST: <u> OK </u>
9	OVER LOAD (typ.)	105%~115%LOAD 180sec 115%~150%LOAD 10 sec Shut down o/p voltage, re-power on to recover	IP: 12VDC OP: TESTING SW:ON Ta:25°C	(1). <u> 105 </u> %~ <u> 112 </u> % <u> 180.2 </u> sec (2). <u> 118 </u> %~ <u> 145.3 </u> % <u> 10.08 </u> sec Shut down o/p voltage, re-power on to recover

CONTROL FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	REMOTE CONTROL	(1).Power ON-OFF remote control by front panel dry contact connector (by RELAY) Open : Normal work Short : Remote off (2). IRC3	IP: 12VDC OP: FULL LOAD Ta:25°C	Open : Normal work Short : Remote off (1).TEST: <u> OK </u> (2).TEST: <u> OK </u>

APPLICATION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	LAMP	LAMP: <u> 696 </u> W · turn on <u> OK </u> LAMP: <u> 1126 </u> W · turn on <u> OK </u> LAMP: <u> 1272 </u> W · turn on <u> OK </u>	1. Vin=HIGH LINE 2. O/P=110V/60Hz TEST: <u> OK </u>	
2	INDUCTION MOTOR	<u> 0.5 </u> HP	1. Vin=HIGH LINE 2. O/P=110V/60Hz TEST: <u> OK </u>	
3	SWITCHING POWER SUPPLY	WITH PFC: <u> RSP-1600-48 </u> . O/P= <u> 1224 </u> W	1. Vin=HIGH LINE 2. O/P=110V/60Hz TEST: <u> OK </u>	
		NO PFC: <u> SE-1000-48 </u> . O/P= <u> 502 </u> W	1. Vin=HIGH LINE 2. O/P=110V/60Hz TEST: <u> OK </u>	

COMPONENT WEAFORM TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	
1	DC TO DC Power Transistor (D to S) or (C to E) Peak Voltage	Q101 Rated : 60V /195 A	I/P: high line O/P:V(max)/Freq 60HZ VDS: O/P: (1)Full Load Turn On (2) Output Short (3)O.L.P(2000W) Turn On (4) NO LOAD Turn On (5) Saving mode Ta:25°C	Q101 (1) 39.9V (2) 38.3V (3) 49.2V (4) 38.3V (5) 38.7V	Q105 (1) 44.7V (2) 40.3V (3) 53.8V (4) 37.9V (5) 37.5V
2	DC TO DC Diode Peak Voltage	D 151 Rated : 300V/ 20A	I/P: high line O/P:V(max) /Freq 60HZ O/P: (1)Full Load Turn On (2) Output Short (3)O.L.P(2000W) Turn On (4) NO LOAD Turn On (5) Saving mode Ta:25°C	D151 (1) 267V (2) 293V (3) 269V (4) 275V (5) 271V	D152 (1) 275V (2) 291V (3) 275V (4) 289V (5) 287V
3	DC BUS Capacitor Voltage	C161 Rated : 680 u/ 315 V	I/P: high line O/P:V(max) /Freq 60HZ O/P: (1)Full Load Turn On (2) Output Short (3)O.L.P(2000W) Turn On (4) NO LOAD Turn On (5) Saving mode Ta:25°C	C161 (1) 266V (2) 266V (3) 266V (4) 266V (5) 266V	
4	DC TO AC Power Transistor (D to S) or (C to E) Peak Voltage	Q 1 IKP15N65H5 Rated : 40A / 650 V	I/P: high line O/P:V(max) /Freq 60HZ VDS: O/P: (1)Full Load Turn On (2) Output Short (3)O.L.P(2000W) Turn On (4) NO LOAD Turn On (5) Saving mode Ta:25°C	Q1 (1)283V (2)313V (3)303V (4)283V (5)289V	Q4 (1)316V (2)332V (3)320V (4)324V (5)316V
5	AUX PWM MOS	Q201 Rated : 80 A/ 100 V Q501 Rated : 120 A/ 60 V	I/P: high line O/P:V(max) /Freq 60HZ O/P: (1)Full Load Turn On (2) Output Short (3)O.L.P(2000W) Turn On (4) NO LOAD Turn On (5) Saving mode Ta:25°C	Q201 (1) 65.7V (2) 65.7V (3) 65.7V (4) 65.7V (5) 65.7V	Q501 (1) 34.4V (2) 34.4V (3) 34.4V (4) 34.4V (5) 34.4V
6	Control IC Voltage Test	MCU IC U301 Rated 2.4 V~ 3.6 V AUX IC U201 Rated	I/P: high line O/P:V(max) /Freq 60HZ O/P: (1)Full Load Turn On (2) Output Short (3)O.L.P(2000W) Turn On	U301 (1) 3.39V (2) 3.39V (3) 3.39V (4) 3.39V	U501 (1) 12.56V (2) 12.56V (3) 12.56V (4) 12.56V

	8.2V~30V	(4) NO LOAD Turn On (5) Saving mode Ta:25°C	(5) 3.39V U201 (1) 12.32V (2) 12.32V (3) 12.32V (4) 12.32V (5) 12.32V	(5) 12.56V U81 (1) 5.08V (2) 5.08V (3) 5.08V (4) 5.08V (5) 5.08V
	CHARGE IC U501 Rated -0.3V~20V			
	Gate Driver IC U81 Rated -0.3V~20V			

SAFETY & EMC TEST

SAFETY TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	WITHSTAND VOLTAGE	BAT I/P-AC O/P: 3 KVAC/min AC O/P-FG: 1.5 KVAC/min	BAT I/P-AC O/P: 3.6 KVAC/min AC O/P-FG:1.8 KVAC/min Ta:25°C	BAT I/P-AC O/P: 6.98 mA AC O/P-FG: 5.98 mA NO DAMAGE
2	GROUNDING CONTINUITY	IEC62368 FG(PE) TO CHASSIS OR TRACE < 100 mΩ	40 A / 2min Ta:25°C	3mΩ

E.M.C TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	RADIATION	FCC (except for Type-UN) CLASS A	I/P:12 VDC O/P: :FULL/50% LOAD Ta:25°C	CLASS A
2	E.S.D	EN61000-4-2 AIR : 8KV / Contact : 4KV	I/P: 12VDC O/P:FULL LOAD Ta:25°C	<input checked="" type="checkbox"/> CRITERIA A <input type="checkbox"/> CRITERIA B
3	Test by certified Lab & Test Report Prepare Any contradictions of the test results, please refer to the latest EMC test report			

Reliability Test

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT			
1	TEMPERATURE RISE TEST	MODEL : NTU-1200-112					
		1. ROOM AMBIENT BURN-IN : 2 HRS I/P : 12.5VDC O/P : FULL LOAD Ta= 25.0 °C					
		2. HIGH AMBIENT BURN-IN : 2 HRS I/P : 12.5VDC O/P : FULL LOAD Ta= 35.0 °C					
				NO	Position	ROOM AMBIENT Ta=25.0 °C	HIGH AMBIENT Ta= 35.0 °C
				1	C101	65.9°C	78.0°C
				2	Q108	63.5°C	76.1°C
				3	RTH6	59.4°C	71.3°C
				4	Q101	54.4°C	66.5°C
				5	T501	32.4°C	43.2°C
				6	C531	34.3°C	44.5°C
				7	Q501	37.2°C	47.1°C
				8	Q141	35.6°C	46.1°C
				9	T101 CORE	72.4°C	85.6°C
				10	T101 COIL	84.4°C	102.6°C
				11	LF1	50.0°C	60.4°C
				12	CT1	38.3°C	48.4°C
				13	C161	43.3°C	54.1°C
				14	Q1	79.0°C	89.2°C
				15	RY1	44.8°C	56.7°C
				16	C2	32.1°C	41.9°C
				17	Q2	73.7°C	83.5°C
				18	D151	69.5°C	79.4°C
				19	T202	34.1°C	43.7°C
				20	Q201	37.5°C	47.8°C
				21	L10	58.2°C	69.2°C
				22	R223	49.8°C	61.3°C
				23	C222	40.3°C	51.4°C
				24	D152	62.4°C	72.8°C
				25	TSW1	53.3°C	61.2°C
				26	R213	54.2°C	67.4°C
				27	U201	46.7°C	58.2°C
				28	Q203	40.0°C	51.5°C
				29	U81	38.9°C	50.1°C
				30	R501	38.3°C	48.0°C
				31	U501	36.4°C	46.7°C
				32	D501	36.7°C	46.8°C
				33	R24	65.1°C	74.6°C
				34	U301	36.5°C	47.1°C
		35	U361	35.1°C	46.1°C		
		36	TC1	40.9°C	51.3°C		
		37	TC2	39.8°C	50.2°C		
2	LOW TEMPERATURE TURN ON TEST	TURN ON AFTER 2 HOUR	I/P : 12.5VDC O/P : 100%LOAD Ta= -25 °C	TEST : OK			

3	HIGH HUMIDITY HIGH TEMPERATURE HIGH VOLTAGE TURN ON TEST	AFTER 12 HOURS IN CHAMBER ON CONTROL 35 °C NO DAMAGE	I/P : 16.2VDC O/P : FULL LOAD Ta= 35 °C HUMIDITY= 95 %R.H	TEST : OK
4	STORAGE TEMPERATURE TEST	1. Thermal shock Temperature : -45°C~ +90°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 5 CYCLE 5. Input/Output condition : STATIC		TEST : OK
5	THERMAL SHOCK TEST	1. Thermal shock Temperature : -25°C~ +40°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 10 CYCLE 5. Input/Output condition : 12.5VDC/Full Load		TEST : OK
6	VIBRATION TEST	1 Carton & 1 Set (1) Waveform : Sine Wave (2) Frequency : 10~500Hz (3) Sweep Time : 10min/sweep cycle (4) Acceleration : 4G (5) Test Time : 60min in each axis (X.Y.Z) (6) Ta : 25°C		TEST : OK
7	CAPACITOR LIFE CYCLE	SUPPOSE C101 IS THE MOST CRITICAL COMPONENT (1) I/P: 12.5VDC O/P: FULL LOAD Ta= 25 °C LIFE TIME (2) I/P: 12.5VDC O/P: FULL LOAD Ta= 35 °C LIFE TIME		(1) 141598.4HRS (2) 66517.5HRS
8	MTBF	Conducted by Parts Stress Analysis Prediction 596.7K hrs min. Telcordia TR/SR-332 (Bellcore) ; 62.0K hrs min. MIL-HDBK-217F (25°C)		
9	Ongoing Reliability Test	I/P : 12.5VDC O/P : 80% LOAD TA=50°C Demonstration Mean Time Between Failure : 30,000 hours		

TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	LIUTT		WANGDZ

2018.4.30 GP-A50-F010